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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Inventor: Bailey, Todd C.

App. No.: 10/781,278

GPAU: 1756

Filing Date: 02/18/2004

Examiner: Christopher G. Young

Dkt. No.: PA95-37D13

Conf. No.: 2060

For: Imprint Lithography Template Having a Mold to Compensate
for Material Changes of an Underlying Liquid

INFORMATION DISCLOSURE STATEMENT

Mail Stop AMENDMENT
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

The following information is submitted in compliance with Applicants' duty of disclosure and pursuant to 37 C.F.R. § 1.56, § 1.97 and § 1.98. The undersigned brings the patents, publications, applications or other information identified in the attached:

- ☒ Form(s) PTO/SB/08A and/or PTO/SB/08B or PTO/1449
☐ Other:

to the Examiner's attention in the above-identified application. It is respectfully requested that the cited information be expressly considered during the prosecution of this application, and the references be made of record therein and appear among the "references cited" on any patent to issue therefrom.

For the convenience of the Examiner, references cited in the parent patent application are notated on the form PTO/SB/08A with bold font.

This Information Disclosure Statement is being filed:



before the mailing date of a first Office action on the merits or before the mailing date of a first Office action after the filing of a request for continued examination under § 1.114. Therefore, no fee is believed required.

Applicants do not believe that any additional fees are due, but if the Commissioner believes additional fees are due, the Commissioner is hereby authorized to charge any fees which may be required, or credit any overpayment, to Deposit Account Number 502650.

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I hereby certify that this correspondence is being facsimile transmitted to the USPTO or deposited with the United States Postal Service with sufficient postage as either First Class Mail or Express Mail, in an envelope addressed to the Commissioner for Patents.

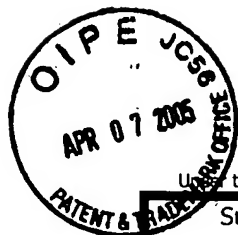
Signed: Katrina Prati

Typed Name: Katrina Prati

Date: 3-30-05

Respectfully Submitted,

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Legal Department
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PTO/SB/08A (08-03)

Approved for use through 7/31/2006. OMB 0651-0031

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**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT***(use as many sheets as necessary)*

Sheet 1 of 8

Complete if Known

APPLICATION NUMBER	10/781,278
FILING DATE	02/18/2004
FIRST NAMED INVENTOR	Sreenivasan et al.
Group Art Unit	1756
Examiner Name	Young, Christopher G.
Attorney Docket Number	PA95-37D13

U.S. PATENT DOCUMENTS

Examiner Initials*	Cite No. ¹	U.S. Patent Document		Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number	Kind Code ² (if known)			

	B1	2002/0098426	A1	Sreenivasan et al.	7/25/2002	
	B2	4,731,155		Napoli et al.	3/15/1988	
	B3	5,425,848		Haisma et al.	6/20/1995	
	B4	5,772,905		Chou	6/30/1998	
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	B6	6,334,960	B1	Willson et al.	1/1/2002	
	B7	6,391,217	B2	Schaffer et al.	5/21/2002	
	B8	6,517,977	B2	Resnick et al.	2/11/2003	
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	B10	10/687,519		Nguyen et al.	10/16/2003	
	B11	2002/0177319	A1	Chou	11/28/2002	
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	B17	5,849,222		Jen et al.	12/15/1998	
	B18	2003/0113638	A1	Mancini et al.	6/19/2003	

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This collection of information is required by 37 CFR 1.97 and 1.98. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 2 hours to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending on the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. **SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.**

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	B19	6,646,662	B1	Nebashi et al.	11/11/2003	
	B20	6,776,094	B1	Whitesides et al.	8/17/2004	
	B21	6,518,189	B1	Chou	2/11/2003	
	B22	2002/0132482	A1	Chou	9/19/2002	
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	B37	2003/0034329	A1	Chou	2/20/2003	
	B38	2002/0042027	A1	Chou et al.	4/11/2002	
	B39	10/666,527		Bailey et al.	9/18/2003	
	B40	6,580,172	B2	Mancini et al.	6/17/2003	
	B41	4,722,878		Watakabe et al.	2/2/1988	
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		Office 3	Number ⁴	Kind Code ⁵ (if known)				

	B57	WO	00/21689		Chou et al.	4/20/2000		
	B58	JP	02-92603		Kurikawa et al.	4/3/1990		
	B59	JP	02-24848		Kamio	1/26/1990		
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	B61	WO	02/07199		Chou	1/24/2002		
	B62	WO	2004/114016		Chou et al.	12/29/2004		
	B63	WO	99/05724		Chou et al.	2/4/1999		
	B64	WO	03/010289		Chou et al.	2/6/2003		
	B65	WO	03/079416		Chou	9/25/2003		
	B66	WO	03/099536		Chou et al.	12/4/2003		

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				Group Art Unit	1756
				Examiner Name	Young, Christopher G.
Sheet	6	of	8	Attorney Docket Number	PA95-37D13

OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²

	B67	Chou, Nanoimprint Lithography and Lithographically Induced Self-Assembly, MRS Bulletin, pp. 512 - 517, 7/1/2001	
	B68	Choi et al., Design of Orientation Stages for Step and Flash Imprint Lithography, Precision Engineering, pp. 192 - 199, 1/1/2001	
	B69	Colburn et al., Step and Flash Imprint Lithography for sub-100 nm Patterning, Proceedings of SPIE Vol. 3997, pp. 453 - 457, 1/1/2000	
	B70	Chou et al., Imprint Lithography with 25-Nanometer Resolution, Science Vol. 272, pp. 85 - 87, 4/5/1996	
	B71	Chou et al., Imprint Lithography with Sub-10 nm Feature Size and High Throughput, Microelectronic Engineering 35, pp. 237 - 240, 1/1/1997	
	B72	Haisma et al., Mold-assisted Nanolithography: A Process for Reliable Pattern Replication, J. Vac. Sci. Technol. B, pp 4124 - 4128, 11/1/1996	
	B73	Chou et al., Imprint of Sub-25 nm Vias and Trenches in Polymers, Appl. Phys. Lett. 67 (21), 11/20/1995	
	B74	Johnson et al., Advances in Step and Flash Imprint Lithography, SPIE Microlithography Conference, 2/23/2003	
	B75	Chou et al., Lithographically Induced Self-assembly of Periodic Polymer Micropillar Arrays, J. Vac. Sci. Technol. B 17 (6), pp. 3197 - 3202, 11/1/1999	

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	B76	Colburn et al., Step and Flash Imprint Lithography: A New Approach to High-Resolution Patterning, Proc. Of SPIE, vol. 3676, 3/1/1999	
	B77	Heidari, Nanoimprint Lithography at the 6 in. Wafer Scale, J. Vac. Sci. Technol. B 18(6), pp. 3557 - 3560, 11/1/2000	
	B78	Translation of Japanese Patent 02-92603	
	B79	Translation of Japanese Patent 02-24848	
	B80	Chou et al., Ultrafast and Direct Imprint of Nanostructures in Silicon, Nature, Col. 417, (June 2002), pp. 835-837, 6/1/2002	
	B81	Chou et al., Nanoimprint Lithography, Journal of Vacuum Science Technology B 14(16), pp. 4129-4133, 11/1/1996	
	B82	Colburn et al., Development and Advantages of Step-and-Flash Lithography, Solid State Technology, 7/1/2001	
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	B84	Colburn et al., Characterization and Modeling of Volumetric and Mechanical Properties for Step and Flash Imprint Lithography Photopolymers, Journal of Vacuum Science Technology. Vol b. 19(6), 11/1/2001	

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	B85	Bailey et al., Step and Flash Imprint Lithography: Defect Analysis, Journal of Vacuum Science, B 19(6), pp. 2806-2810, 11/1/2001	
	B86	Bailey et al., Step and Flash Imprint Lithography: Template Surface Treatment and Defect Analysis, Journal of Vacuum Science, B 18(6), pp. 3572-3577, 11/1/2000	
	B87	Schneider et al., Stripes of Partially Fluorinated Alkyl Chains: Dipolar Langmuir Monolayers	

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